

**Spring Semester**

 **May 2024 Exam Timetable**

**School of Engineering**

**Department of Electronic Engineering**

**Important notice for all students:**

**Timetables are subject to change - revisit this site for updates**

* **Please ensure that you are in possession of a current photographic ID card**

**If not, you should contact the Student Hub as soon as possible**

* **NBA = National Basketball Arena**
* **TU Dublin – Tallaght Campus**

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| **BACHELOR OF ENGINEERING – ELECTRONIC ENGINEERING GENERAL ENTRY** **– AB INITIO - YEAR 1** |
| **Timetable** | **Course Code** | **TA\_ELEGEN\_D - Year 1** | **FT ONLY** |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 2** | **15:30 – 17:30** | **NBA** |
| **10th May** | **Friday** | **Interactive Embedded Systems** | **09:30 – 11:30** | **NBA** |
| **13th May** | **Monday** | **Digital Systems 1** | **19:00 – 21:00** | **NBA** |
| **15th May** | **Wednesday** | **Analogue Electronics**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Electrical Circuits 2** | **12:30 – 14:30** | **NBA**  |

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| **BACHELOR OF ENGINEERING(HONS) – ELECTRONIC ENGINEERING GENERAL ENTRY** **– YEAR 1** |
| **Timetable** | **Course Code** | **TA\_ELEGEN\_B - Year 1** | **FT ONLY** |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 2** | **15:30 – 17:30** | **NBA** |
| **10th May** | **Friday** | **Interactive Embedded Systems** | **09:30 – 11:30** | **NBA** |
| **13th May** | **Monday** | **Digital Systems 1** | **19:00 – 21:00** | **NBA** |
| **15th May** | **Wednesday** | **Analogue Electronics**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Electrical Circuits 2** | **12:30 – 14:30** | **NBA**  |

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| **HIGHER CERTIFICATE IN ENGINEERING – ELECTRONIC & INFORMATION ENGINEERING - YEAR 1** |
| **Timetable** | **Course Code** | **TA\_EELIE\_C - Year 1** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 2** | **15:30 – 17:30** | **NBA** |
| **10th May** | **Friday** | **Interactive Embedded Systems** | **09:30 – 11:30** | **NBA** |
| **13th May** | **Monday** | **Digital Systems 1** | **19:00 – 21:00** | **NBA** |
| **15th May** | **Wednesday** | **Analogue Electronics**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Electrical Circuits 2** | **12:30 – 14:30** | **NBA**  |

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| **HIGHER CERTIFICATE IN ENGINEERING – ELECTRONIC ENGINEERING - YEAR 1** |
| **Timetable** | **Course Code** | **TA\_EELEC\_C - Year 1**  | **PT ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **10th May** | **Friday** | **Interactive Embedded Systems** | **09:30 – 11:30** | **NBA** |
| **13th May** | **Monday** | **Physics** | **19:00 – 21:00** | **NBA** |
| **15th May** | **Wednesday** | **Analogue Electronics**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Electrical Circuits 2** | **12:30 – 14:30** | **NBA**  |

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| **HIGHER CERTIFICATE IN ENGINEERING – ELECTRONIC & INFORMATION ENGINEERING** |
| **Timetable** | **Course Code** | **TA\_EELIE\_C - Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Microprocessor Fundamentals**  | **09:30 – 11:30** | NBA |
| **9th May** | **Thursday** | **Solid State Electronics (e)** | **15:30 – 17:30** | NBA |
| **9th May** | **Thursday** | **GUI Development (e)** | **15:30 – 17:30** | NBA |
| **13th May** | **Monday** | **Mathematics 4** | **09:30 – 11:30** | NBA |
| **15th May** | **Wednesday** | **Control Systems**  | **15:30 – 17:30** | NBA |
| **16th May** | **Thursday** | **Routers & Switches** | **15:30 – 17:30** | NBA |

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| **HIGHER CERTIFICATE IN ENGINEERING - ELECTRONIC ENGINEERING - YEAR 2** |
| **Timetable** | **Course Code** | **TA\_EELEC\_C** | **Part Time Only** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Digital Systems 2** | **09:30 – 11:30** | **NBA**  |
| **15th May** | **Wednesday** | **Control Systems**  | **15:30 – 17:30** | NBA |
|  |  | **Out of Semester** |  |  |
| **9th May** | **Thursday** | **JAVA** | **15:30 – 17:30** | NBA  |
| **7th May** | **Tuesday** | **Digital Systems 2** | **09:30 – 11:30** | NBA |

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| **BACHELOR OF ENGINEERING - ELECTRONIC ENGINEERING – AB INITIO - YEAR 2** |
| **Timetable** | **Course Code** | **TA\_EAELE\_D - Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Microprocessor Fundamentals**  | **09:30 – 11:30** | **NBA** |
| **9th May** | **Thursday** | **Solid State Electronics (e)** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **GUI Development (e)** | **15:30 – 17:30** | **NBA** |
| **13th May** | **Monday** | **Mathematics 4** | **09:30 – 11:30** | **NBA** |
| **15th May** | **Wednesday** | **Control Systems**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Routers & Switches** | **15:30 – 17:30** | **NBA** |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING – AB INITIO- YEAR 2** |
| **Timetable** | **Course Code** | **TA\_EAELE\_B - Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Microprocessor Fundamentals**  | **09:30 – 11:30** | **NBA** |
| **9th May** | **Thursday** | **Solid State Electronics (e)** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **GUI Development (e)** | **15:30 – 17:30** | **NBA** |
| **13th May** | **Monday** | **Mathematics 4** | **09:30 – 11:30** | **NBA** |
| **15th May** | **Wednesday** | **Control Systems**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Routers & Switches** | **15:30 – 17:30** | **NBA**  |

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| **BACHELOR OF ENGINEERING – ENGINEERING SOFTWARE –AB INITIO YEAR 2** |
| **Timetable** | **Course Code** | **EAENS\_D Year 2** | **Full Time ONLY** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Microprocessor Fundamentals**  | **09:30 – 11:30** | **NBA** |
| **9th May** | **Thursday** | **Solid State Electronics (e)** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **GUI Development (e)** | **15:30 – 17:30** | **NBA** |
| **13th May** | **Monday** | **Mathematics 4** | **09:30 – 11:30** | **NBA** |
| **15th May** | **Wednesday** | **Control Systems**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Routers & Switches** | **15:30 – 17:30** | **NBA**  |

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| **BACHELOR OF ENGINEERING (HONS) – ENGINEERING SOFTWARE –AB INITIO** |
| **Timetable** | **Course Code** | **TA\_EAENS\_B Year 2** |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Microprocessor Fundamentals**  | **09:30 – 11:30** | **NBA** |
| **9th May** | **Thursday** | **Solid State Electronics (e)** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **GUI Development (e)** | **15:30 – 17:30** | **NBA** |
| **13th May** | **Monday** | **Mathematics 4** | **09:30 – 11:30** | **NBA** |
| **15th May** | **Wednesday** | **Control Systems**  | **15:30 – 17:30** | **NBA** |
| **16th May** | **Thursday** | **Routers & Switches** | **15:30 – 17:30** | **NBA**  |

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| **BACHELOR OF ENGINEERING - ELECTRONIC ENGINEERING - YEAR 3**  |
| **Timetable** | **Course Code** | **TA\_EELEC\_D - Year 3** | **Full Time & Part Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 6** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **Electronic Circuits & Filters** | **12:30 – 14:30** | **NBA** |
| **11th May** | **Saturday** | **Digital Design with Verilog** | **09:30 – 11:30** | **NBA** |
| **13th May** | **Monday** | **Analysis of Analogue Communications** | **19:00 – 21:00** | **NBA** |
| **14th May** | **Tuesday** | **Embedded Systems** | **19:00 – 21:00** | **NBA** |
| **15th May** | **Wednesday** | **Network Design** | **15:30-17:30** | **NBA** |
| **16th May** | **Thursday** | **Power Engineering** | **15:30 – 17:30** | **NBA**  |

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| **BACHELOR OF ENGINEERING - ELECTRONIC ENGINEERING – AB INITIO - YEAR *3***  |
| **Timetable** | **Course Code** | **TA\_EAELE\_D - Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 6** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **Electronic Circuits & Filters** | **12:30 – 14:30** | **NBA** |
| **11th May** | **Saturday** | **Digital Design with Verilog** | **09:30 – 11:30** | **NBA** |
| **13th May** | **Monday** | **Analysis of Analogue Communications** | **19:00 – 21:00** | **NBA** |
| **14th May** | **Tuesday** | **Embedded Systems** | **19:00 – 21:00** | **NBA** |
| **16th May** | **Thursday** | **Power Engineering** | **15:30 – 17:30** | **NBA**  |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING – AB INITIO – YEAR *3***  |
| ***Timetable*** | ***Course Code*** | **TA\_EAELE\_B - Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Mathematics 6** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **Electronic Circuits & Filters** | **12:30 – 14:30** | **NBA** |
| **11th May** | **Saturday** | **Digital Design with Verilog** | **09:30 – 11:30** | **NBA** |
| **13th May** | **Monday** | **Analysis of Analogue Communications** | **19:00 – 21:00** | **NBA** |
| **14th May** | **Tuesday** | **Embedded Systems** | **19:00 – 21:00** | **NBA** |
| **16th May** | **Thursday** | **Power Engineering** | **15:30 – 17:30** | **NBA**  |

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| **BACHELOR OF ENGINEERING - ENGINEERING SOFTWARE - YEAR 3** |
| **Timetable** | **Course Code** | **TA\_ EAENS\_D Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 6** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **Data Structures & Algorithms** | **09:30 – 11:30** | **NBA** |
| **14th May** | **Tuesday** | **Embedded Systems** | **19:00 – 21:00** | **NBA**  |
| **15th May** | **Wednesday** | **Management Practice** | **09:30 – 11:45** | **NBA** |

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| **BACHELOR OF ENGINEERING (HONS) - ENGINEERING SOFTWARE - YEAR 3** |
| **Timetable** | **Course Code** | **TA\_ EAENS\_B Year 3** |  |  |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 6** | **15:30 – 17:30** | **NBA** |
| **9th May** | **Thursday** | **Data Structures & Algorithms** | **09:30 – 11:30** | **NBA** |
| **14th May** | **Tuesday** | **Embedded Systems** | **19:00 – 21:00** | **NBA**  |
| **15th May** | **Wednesday** | **Management Practice** | **09:30 – 11:45** | **NBA** |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING - YEAR 4**  |
| **Timetable** | **Course Code** | **TA\_EELEC\_B - Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 8** | **09:30 – 11:45** | 223/225 |
| **8th May** | **Wednesday** | **Software Development 2 (e)** | **09:30 – 11:45** | 223/225 |
| **10th May** | **Friday** | **Operating Systems (e)** | **12:30 – 14:45** | 040/042 |
| **10th May** | **Friday** | **Digital Microprocessor Design (e)** | **12:30 – 15:45** | 142/144 |
| **11th May** | **Saturday** | **Network Engineering (e)** | **12:30 – 14:45** | 223/225 |
| **13th May** | **Monday** | **Analogue IC Design (e)**  | **15:30 – 17:45** | 040/042 |
| **14th May** | **Tuesday** | **Digital Signal Processing 2** | **09:30 – 11:45** | 040/042 |
| **15th May** | **Wednesday** | **Management Practice** | **09:30 – 11:45** | 223/225 |
| **16th May** | **Thursday** | **Nanoscale Devices (e)** | **12:30 – 14:45** | 223/225 |
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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING - YEAR 4**  |
| **Timetable** | **Course Code** | **TA\_EAELE\_B Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 8** | **09:30 – 11:45** | 223/225 |
| **8th May** | **Wednesday** | **Software Development 2 (e)** | **09:30 – 11:45** | 223/225 |
| **10th May** | **Friday** | **Operating Systems (e)** | **12:30 – 14:45** | 040/042 |
| **10th May** | **Friday** | **Digital Microprocessor Design (e)** | **12:30 – 15:45** | 142/144 |
| **11th May** | **Saturday** | **Network Engineering (e)** | **12:30 – 14:45** | 223/225 |
| **13th May** | **Monday** | **Analogue IC Design (e)** | **15:30 – 17:45** | 040/042 |
| **14th May** | **Tuesday** | **Digital Signal Processing 2** | **09:30 – 11:45** | 040/042 |
| **15th May** | **Wednesday** | **Management Practice** | **09:30 – 11:45** | 223/225 |
| **16th May** | **Thursday** | **Nanoscale Devices (e)** | **12:30 – 14:45** | 223/225 |

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| **BACHELOR OF ENGINEERING (HONS) - ELECTRONIC ENGINEERING - YEAR 4**  |
| **Timetable** | **Course Code** | **TA\_ ECELE\_B Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 8** | **09:30 – 11:45** | 223/225 |
| **8th May** | **Wednesday** | **Software Development 2 (e)** | **09:30 – 11:45** | 223/225 |
| **10th May** | **Friday** | **Operating Systems (e)** | **12:30 – 14:45** | 040/042 |
| **10th May** | **Friday** | **Digital Microprocessor Design (e)** | **12:30 – 15:45** | 142/144 |
| **11th May** | **Saturday** | **Network Engineering (e)** | **12:30 – 14:45** | 223/225 |
| **13th May** | **Monday** | **Analogue IC Design (e)** | **15:30 – 17:45** | 040/042 |
| **14th May** | **Tuesday** | **Digital Signal Processing 2** | **09:30 – 11:45** | 040/042 |
| **15th May** | **Wednesday** | **Management Practice** | **09:30 – 11:45** | 223/225 |
| **16th May** | **Thursday** | **Nanoscale Devices (e)** | **12:30 – 14:45** | 223/225 |

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| **BACHELOR OF ENGINEERING (HONS) - ENGINEERING SOFTWARE - YEAR 4** |
| **Timetable** | **Course Code** | **TA\_EAENS\_B - Year 4** | **15 Minutes Reading Time** |
| **DATE** | **DAY** | **SUBJECT** | **TIME** | **LOCATION**  |
| **7th May** | **Tuesday** | **Mathematics 8** | **09:30 – 11:45** | 223/225 |
| **8th May** | **Wednesday** | **Software Development 2** | **09:30 – 11:45** | 223/225 |
| **10th May** | **Friday** | **Operating Systems** | **12:30 – 14:45** | 040/042 |
| **11th May** | **Saturday** | **Machine Learning (e)** | **12:30 – 14:45** | 223/225 |
| **11th May** | **Saturday** | **Network Engineering (e)** | **12:30 – 14:45** | 223/225 |
| **15th May** | **Wednesday** | **Internet of Things**  | **09:30 – 11:45** | 223/225 |

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| **BACHELOR OF ENGINEERING IN INDUSTRIAL ELECTRICAL ENGINEERING**  |
| **Timetable** | **Course Code** | **TA\_EIELE\_D (stage 1)** |  |
| **DATE** | **DAY** | **SUBJECT**  | **TIME** | **LOCATION**  |
| **14th May** | **Tuesday** | **Electrical Machines**  | **19:00 – 21:00** | NBA |

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| **BACHELOR OF ENGINEERING IN INDUSTRIAL ELECTRICAL ENGINEERING**  |
| **Timetable** | **Course Code** | **TA\_EIELE\_D (stage 2)** |  |
| **DATE** | **DAY** | **SUBJECT**  | **TIME** | **LOCATION** |
| **14th May** | **Tuesday** | **Process Instrumentation & Calibration**  | **19:00 – 21:00** | NBA |

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| **HIGHER CERTIFICATE IN ENGINEERING IN TELECOMMUNICATIONS & DATA NETWORKS**  |
| **Timetable** | **Course Code** | **TA\_ ETELE\_C** |  |
| **DATE** | **DAY** | **SUBJECT**  | **TIME** | **LOCATION** |
| **7th May** | **Tuesday** | **Electrical Principles (Stage 1)** | **19:00 – 21:00** | NBA |
| **8th May** | **Wednesday** | **Optical Fibre Maintenance (Stage 2)** | **19:00 – 21:00** | NBA |
| **14th May** | **Tuesday** | **Broadband & Wireless Skills (Stage 2)** | **19:00 – 21:00** | NBA |

**Please ensure you go to the room/area allocated for your course code on your ID Card**